Seminars and Workshops Organized

SI. No.	Module description	Any other contributory institute/ industry	Develo ped/ organi zed by	Duration	Resource persons	Target audience	Usage and citation, etc.
1	A three day workshop on "Design Verification using Verilog and System Verilog"	M/s. EDULIFE India, (A Leading Research and Innovation Driven Institution)	Dr. B G Shivale elavathi , Prof. ECE Depart ment	26 th & 27 th November 2015 and 11 th December 2015	Sanju Singh,Project Manager, Edulife India Mulla Syed, Head System Verilog, Edulife India Praveen Tripathi, Technical Director, Edulife India	1st and 3rd semester M.Tech Students (VLSI Design and Embedded Systems)	 Students learn fundamentals of Verilog and System Verilog. At the end of three days student will be in a position to develop the verification plan after structured study of design specification. The student will also develop the verification architecture and also the test bench of the basic design protocols. www.edulifeindia.com
2	VLSI Design Course: • Layout Design • Physical design	Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road, Mary Hill, Mangalore, Karnataka- 575008	Dr. B G Shivale elavathi , Prof. ECE Depart ment	4 months 18 th January to 18 th May 2013	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation. Off campus placements: till now around 4 students are placed in core electronics /VLSI industries. Slides & documents shared at: www.kanadatech.com
3	VLSI Design Course: Layout Design Physical design Add-on Courses(Linux, perl, Skill, TCL)	Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road, Mary Hill, Mangalore, Karnataka- 575008	Dr. B G Shivale elavathi , Prof. ECE Depart ment	9 months September 2013 to May 2014	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation. Off campus placements: till now around 8 students are placed in core electronics /VLSI industries. Slides & documents shared at: www.kanadatech.com

4	VLSI Design Course: Layout Design Physical design Add-on Courses(Linux, perl, Skill, TCL)	Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road, Mary Hill, Mangalore, Karnataka- 575008	Dr. B G Shivale elavathi , Prof. ECE Depart ment	9 months September 2014 to May 2015	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation. Off campus placements. Slides & documents shared at: www.kanadatech.com
5	VLSI Design Course: • Linux Scripting course	Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road, Mary Hill, Mangalore, Karnataka- 575008	Dr. B G Shivale elavathi , Prof. ECE Depart ment	2 months April & May 2015	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation. Off campus placements. Slides & documents shared at: www.kanadatech.com
6	VLSI Design Course: • Layout Design	Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road, Mary Hill, Mangalore, Karnataka- 575008	Dr. B G Shivale elavathi , Prof. ECE Depart ment	20 th June 2015-11 th Dec 2015	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation. Off campus placements. Slides & documents shared at: www.kanadatech.com
7	VLSI Design Course: • Physical design	Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road,	Dr. B G Shivale elavathi , Prof. ECE Depart ment	8 th Aug 2015-Dec 2015	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation.

8	VLSI Design Course: • Layout Design • Physical design • Add-on Courses(Linux, perl, Skill, TCL)	Mary Hill, Mangalore, Karnataka- 575008 Kanada Technologies (inspiring innovations) Kuldev, 2- 104/14A, Gandhiji Road, Mary Hill, Mangalore, Karnataka- 575008	Dr. B G Shivale elavathi , Prof. ECE Depart ment	6 months December 2015 to May 2016	Mr. Narasimha Kamath, Mr. Ravi Siddanath and team Kanada Technologies	M.Tech students (VLSI Design and Embedded Systems)	 Off campus placements: till now around 10 students are placed in core electronics /VLSI industries. Slides & documents shared at: www.kanadatech.com Theory and lab exercises for the course will be useful for UG & PG students to carry out their final year projects. The training will prepare the students for the industry requirements. Resume review and resource for interview preparation. Off campus placements: around 16 students placements are ongoing for placements in core electronics /VLSI industries. Slides & documents shared at: www.kanadatech.com
9	Business Plan	FEDERATION OF KARNATAKA CHAMBERS OF COMMERCE AND INDUSTRY(FKC CI), BANGALORE	Dr.Sid desh. G.K., Assoc. Prof.	30.11.201 4 (2014- 2015)	MANTHAN -2015, FKCCI	ADITHYA RANGAN C K and SUSHIKSHI TH H. GOWDA, M.Tech students (VLSI Design and Embedded Systems)	Topic Title: Cooperation in Heterogeneous Swarm Environment using Flying Robots. Vision: To develop a simple but a powerful swarm environment which finds application in disaster management, agricultural foraging, rescue operations in mining and battle field Swarm Robotics is the study of robotic systems consisting of a large group of relatively small and simple robots that interact and cooperate with each other in order to jointly solve tasks that are outside their own individual capabilities. The robots that make up a swarm robotic system are usually homogeneous. Heterogeneous robotic systems are considered if they consist of separate group (Sub-swarms) of homogeneous robots. swarm robotics can be suited to tasks that demand cheap designs, for instance mining tasks or agricultural foraging tasks. www.manthan.fkcci.org